

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) An image processing method for providing interpolation processing of pixel data in accordance with an enumerated value of a counter counting a reference clock in a unit which is composed of a prescribed number of pixel data, comprising the steps of:

determining if the pixel data corresponds to a pseudo gray-scale image or a bi-level image;

keeping a final enumerated value of the counter for a former unit instead of resetting the final enumerated value if the pixel data corresponds to the pseudo gray-scale image, while still or resetting the counter a latch circuit at the end of the former unit if the pixel data corresponds to the bi-level image; and,

carrying out a counting processing at a beginning of a current unit with a consecutive enumerated value from the kept final enumerated value if the pixel data corresponds to the pseudo gray-scale image.

2. (Original) The image processing method as in claim 1, wherein one unit corresponds to the pixel data in one scanning line.

3. (Original) The image processing method as in claim 1 further including the steps of:

storing the pixel data in a memory in synchronization with a write clock;

reading out the pixel data from the memory in synchronization with a read clock; and

interpolating the pixel data by generating the read clock through thinning out the write clock in accordance with the enumerated value of the counter.

4. (Original) The image processing method as in claim 3, wherein one or more prescribed number of clocks are thinned out from the write clocks in accordance with a value set in a register.

5. (Currently Amended) An image processing method for thinning out pixel data according to an enumerated value of a counter which counts a reference clock at every one unit that is composed of a prescribed number of pixel data, comprising the steps of:

determining if the pixel data corresponds to a pseudo gray-scale image or a bi-level image;

keeping a final enumerated value of the counter for a former unit instead of resetting the final enumerated value if the pixel data corresponds to the pseudo gray-scale image, while still or resetting the counter a latch circuit at the end of the former unit if the pixel data corresponds to the bi-level image; and,

carrying out a counting processing at a beginning of a current unit with a consecutive enumerated value from the kept final enumerated value if the pixel data corresponds to the pseudo gray-scale image.

6. (Original) The image processing method as in claim 5, wherein one unit corresponds to the pixel data in one scanning line.

7. (Original) The image processing method as in claim 5 further including the steps of

storing the pixel data in a memory in synchronization with a write clock;

reading out the pixel data from the memory in synchronization with a read clock; and

thinning out the pixel data by generating the write clock through thinning out the read clock in accordance with the enumerated value of the counter.

8. (Original) The image processing method of claim 7, wherein one or more specified clocks are thinned out from the read clock in accordance with a value set in a register.

9-20. (Canceled).